



ITF/JP

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: W. McKinzie

Serial No.: 10/828,542

Conf. No.: 3879

Customer No.: 33123

Filed: April 19, 2004

For: **SYSTEMS AND METHODS FOR
BLOCKING MICROWAVE
PROPAGATION IN PARALLEL
PLATE STRUCTURES
UTILIZING CLUSTER VIAS**

Art Unit: 2817

Examiner: Seungsook Ham

CERTIFICATE OF MAILING

I hereby certify that this correspondence and the attached papers are being deposited with the United States Postal Service with sufficient postage as first class mail on the date indicated below and addressed to:

PO Box 1450
Alexandria, VA 22313-1450

12/13/05
Date

Michelle Molendo
Signature

**INFORMATION DISCLOSURE STATEMENT
IN ACCORDANCE WITH 37 C.F.R. §§ 1.97-1.98**

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In accordance with the duty of disclosure imposed by 37 C.F.R. § 1.56 to inform the Office of all references known by Applicant or Applicant's representative that may be material to the examination of the subject application, Applicant's representative hereby provides this Information Disclosure Statement that is prepared in accordance with 37 C.F.R. §§ 1.97-1.98. Form PTO/SB/08 (4 pages) and a copy of the cited non patent literature is provided herewith in connection with the above-captioned application. The cited references that comprise issued U.S. patents are not attached, per the Notice of waiver published in the Official Gazette on August 5, 2003.

The documents listed on the attached Form PTO/SB/08 include communications from a foreign patent office in connection with a counterpart foreign application. Copies of the communications, International Search Report for International PCT Application PCT/US05/13235 and International Search Report for International PCT Application

PCT/US05/06785, are also provided. All documents listed on the attached Form PTO/SB/08 are in the English language, hence, in accordance with the requirements of 37 C.F.R. § 1.98, as amended effective March 16, 1992, no further explanation of the listed items is necessary.

Although these documents and information are made known to the Patent and Trademark Office in compliance with Applicants' duty of disclosure, such disclosure is not to be construed as an admission by Applicants or Applicants' representative that any of the references, singly or in any combination thereof, is effective as prior art against the subject application. In accordance with 37 C.F.R. § 1.97(h), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(b) exists.

Applicants respectfully request that the Examiner review the foregoing references and request that they be made of record in the file history of the above-captioned application.

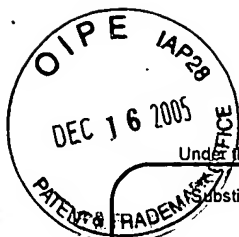
A check in the amount of \$180.00 is enclosed as required under 37 CFR 1.17(p). The Commissioner is hereby authorized to charge any fee, including any submitted herewith if the attached check(s) is in the wrong amount or otherwise improper or missing, that may be due in connection with this and the attached papers, or with this application during its entire pendency to or to credit any overpayment to Deposit Account No. 50-1213. A duplicate of this sheet is enclosed.

Respectfully submitted,
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By: 

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SD 764240 v111/29/05 5:00 PM (42372.0004)



PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031
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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/828,542
Filing Date	04/19/2004
First Named Inventor	W. McKinzie
Art Unit	2918
Examiner Name	Seungsook Ham
Attorney Docket Number	42372-0004B

Sheet 1 of 4

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	A	US- 6,538,538	03-25-2003	Hreish et al.	
	B	US- 6,075,485	06-13-2000	Lilly et al.	
	C	US- 6,061,025	05-09-2000	Jackson et al.	
	D	US- 6,262,495	07-17-2001	Yablonovitch et al.	
	E	US- 6,411,261	06-25-2002	Lilly	
	F	US- 6,476,771	11-05-2002	McKinzie, III	
	G	US- 6,501,427	12-31-2002	Lilly et al.	
	H	US- 6,512,494	01-28-2003	Diaz et al.	
	I	US- 6,525,695	02-25-2003	McKinzie, III	
	J	US- 6,542,342	04-01-2003	Hayashi et al.	
	K	US- 6,567,048	05-20-2003	McKinzie et al.	
	L	US- 6,590,531	07-08-2003	McKinzie, III et al.	
	M	US- 6,670,932	12-30-2003	Diaz et al.	
	N	US- 6,646,605	11-11-2003	McKinzie, III et al.	
	O	US- 6,690,327	02-10-2004	McKinzie, III et al.	
	P	US- 6,768,476	07-27-2004	Lilly et al.	
	Q	US- 6,774,866	08-10-2004	McKinzie, III et al.	
	R	US- 6,774,867	08-10-2004	McKinzie, III et al.	
	S	US- 6,831,602	12-14-2004	McKinzie, III et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Sheet

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Application Number	10/796,398
Filing Date	03/08/2004
First Named Inventor	W. McKinzie
Art Unit	2817
Examiner Name	Seungsook Ham
Attorney Docket Number	42372-0004

U. S. PATENT DOCUMENTS

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		Number-Kind Code ² (if known)			
	T	US- 6,867,746	03-15-2005	Mendolia et al.	
	U	US- 6,888,316	05-03-2005	Seo et al.	
	V	US- 6,897,831	05-24-2005	McKinzie, III et al.	
	W	US- 6,906,674	06-14-2005	McKinzie, III et al.	
	X	US- 6,917,343	07-12-2005	Sanchez et al.	
	Y	US- 6,933,895	08-23-2005	Mendolia et al.	
	Z	US- 6,937,192	08-30-2005	Mendolia et al.	
	AA	US- 2002/0183013 A1	12-05-2002	Auckland et al.	
	AB	US- 2003/0025637 A1	02-06-2003	Mendolia et al.	
	AC	US- 2003/0043071 A1	03-06-2003	Lilly et al.	
	AD	US- 2003/0071763 A1	04-17-2003	McKinzie, III et al.	
	AE	US- 2003/0137457 A1	07-24-2003	McKinzie, III et al.	
	AF	US- 2005/0029632 A1	02-10-2005	McKinzie, III et al.	
		US-			
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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>			Complete if Known		
			Application Number	10/828,542	
			Filing Date	04/19/2004	
			First Named Inventor	W. McKinzie	
			Art Unit	2918	
			Examiner Name	Seungsook Ham	
Sheet	3	of	4	Attorney Docket Number	42372-0004B

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AG	International Search Report dated April 19, 2005, for International Application No. PCT/US05/13235.	
	AH	T. Lee, "A Vertical Leap for Microchips", Scientific American, January 2002, pp. 52-59.	
	AI	International Search Report dated October 21, 2005, for International Application No. PCT/US05/06784.	
	AJ	Abhari et al., "Suppression of Parallel-Plate Noise in High Speed Circuits Using a Metallic Electromagnetic Band-Gap Structure", 2002 IEEE MTT-S Intl. Microwave Symp. Digest, pp. 493-496, Seattle, WA, June 2-7, 2002.	
	AK	Daniel R. Sievenpiper, "High-Impedance Electromagnetic Surfaces," dissertation, UCLA Electrical Engineering Dept., filed January 1999.	
	AL	Abhari et al., "Metallo-Dielectric Electromagnetic Bandgap Structures for Suppression and Isolation of the Parallel-Plate Noise in High Speed Circuits," IEEE Trans. MTT, Vol. 51, No. 6, June 2003.	
	AM	Kamgaing et al., "High-Impedance Electromagnetic Surfaces for Parallel-Plate Mode Suppression in High Speed Digital Systems", IEEE 11th Topical Meeting on Electrical Performance of Electronic Packaging (EPEP), Monterey, CA, Oct. 21-23, 2002.	

Examiner Signature	Date Considered
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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			Examiner Name	Seungsook Ham	
Sheet	4	of	4	Attorney Docket Number	42372-0004B

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AN	Kamgaing et al., "A Novel Power Plane with Integrated Simultaneous Switching Noise Mitigation Capability Using High Impedance Surface," IEEE Microwave and Wireless Components Letter, Vol. 13, No. 1, January 2003.	
	AO	Kamgaing et al., "Inductance-Enhanced High-Impedance Surfaces for Broadband Simultaneous Switching Noise Mitigation in Power Planes," 2003 IEEE MTT-S Intl. Microwave Symp. Digest, pp. 2165-2168, Philadelphia, PA, June 8-13, 2003.	
	AP	Kamgaing et al., "Development and Application of Physics-based Compact Models for High-Impedance Electromagnetic Surfaces Integrated in a Power Plane Configuration", IEEE Intl. Antennas and Propagation Symp., Vol. 4, pp. 442-445, Columbus, OH, June 22-27, 2003.	

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